

FIG. 1 is a block diagram of a system architecture showing two PBXs (PBX1 and PBX2) connected via a LAN. Each PBX contains a VM (VM1 and VM2), a NM (NM1 and NM2), a ST (ST1 and ST2), a W (W1 and W2), and a ZU (ZU1A, ZU1B, ZU2A, ZU2B). External gateways (EG1 and EG2) are connected to the VMs.

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FIG 1

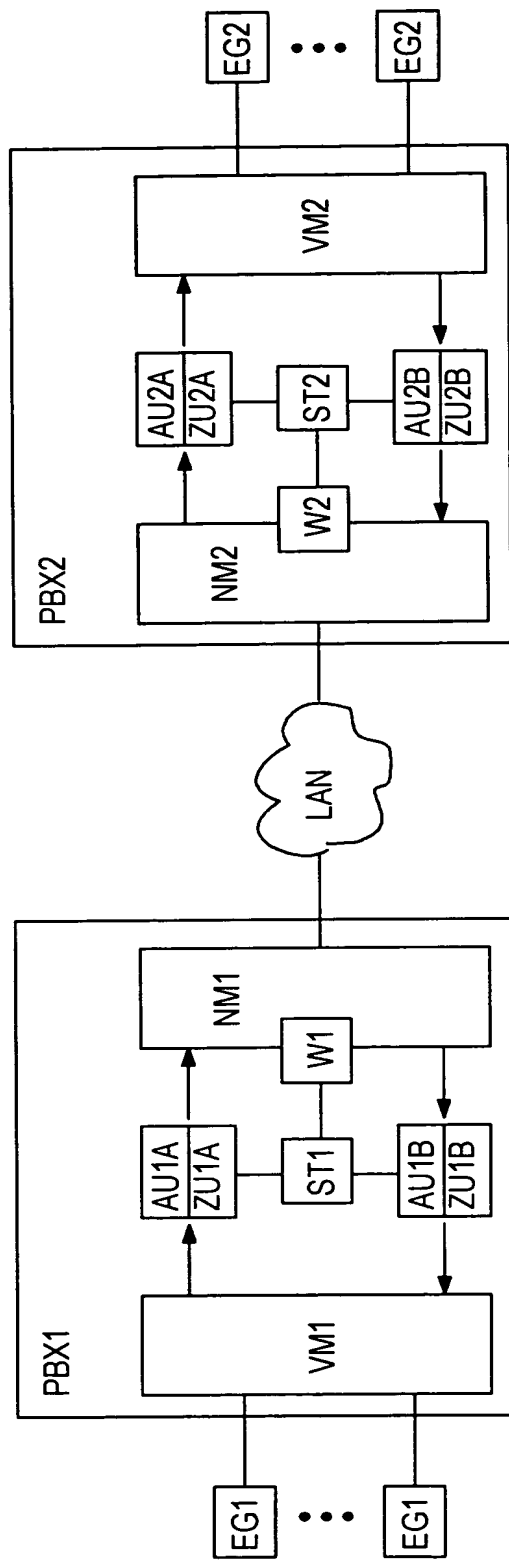


FIG 2

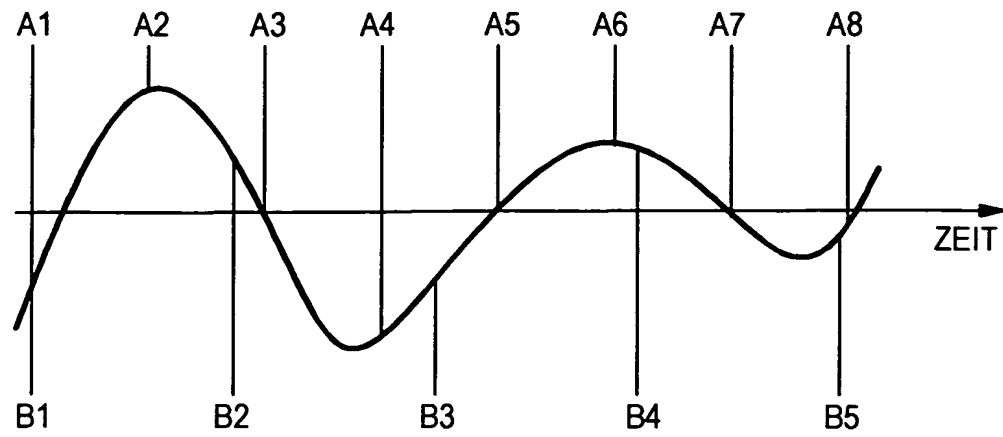


FIG 3

